LAP6 Rec'd PCT/PTO 19 JUL 2006

PATENT ATTORNEY DOCKET NO. 0074/075001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takuji MAEDA et al.

Art Unit: Examiner:

Application No.: filed concurrently Title: SEMICONDUCTOR MEMORY DEVICE AND ITS CONTROL METHOD

International Application No.: PCT/JP2005/000832 International Filing Date : January 24, 2005 Priority Date : January 26, 2004 U.S. Filing Date : July 19, 2006

Mail Stop PCT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the examination for this application, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 4 of this paper.

Remarks begin on page 5 of this paper.